



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tu Docket No.: TSM03-0188  
Patent No.: 6,853,024 B1 Serial No.: 10/679,098  
Issue Date: 02/08/2005 Filed: October 3, 2003  
For: Self-Aligned MIM Capacitor Process for Embedded DRAM

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Respectfully submitted,

*Natalie Swider*  
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Legal Assistant

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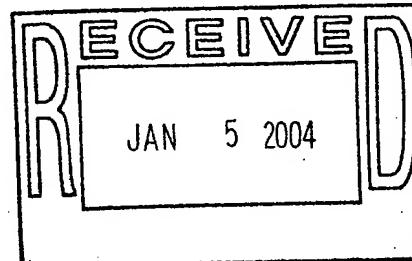
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/679,098	10/03/2003	2811	770	TSM03-0188	17	19	3

25962  
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## Applicant(s)

Kuo-Chi Tu, Hsin-Chu, TAIWAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted: 12/30/2003

Projected Publication Date: 04/07/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Self-aligned MIM capacitor process for embedded DRAM

## Preliminary Class

257

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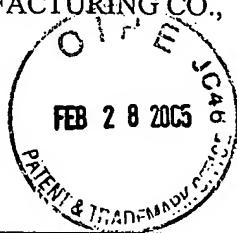
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Kristy Engeldahl	(Depositor's name)
<i>Kristy Engeldahl</i>	(Signature)
January 5, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/679,098	10/03/2003	Kuo-Chi Tu	TSM03-0188	2276

TITLE OF INVENTION: SELF-ALIGNED MIM CAPACITOR PROCESS FOR EMBEDDED DRAM

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$1400	\$300	\$1670 \$1700 01/07/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
NGO, NGAN V	2814	257-301000			

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1 Slater & Matsil, L.L.P.

2 \_\_\_\_\_

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Company, Ltd.

Hsin-Chu, Taiwan

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